

MAY-01-2002 WED 02:50 PM CONLEY ROSE & TAYON

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P. 08

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PATENT

(5500-75900/TT4860)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

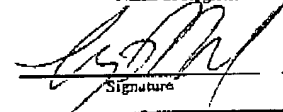
Application No.. 10/023,310
Filed: December 17, 2001
Inventor(s):
Chetana N. Keltcher
William A. Hughes
Michael T. Clark
Bruce R. Holloway
Title: Using Microcode to Correct
ECC Errors in a Processor

§ Examiner: Unknown
§ Group/Art Unit: Unknown
§ Atty. Dkt. No: 5500-75900
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, Washington, DC 20231, on the date indicated below.

Lawrence J. Merkel

Name of Registered Representative


Signature

January 16, 2002

Date

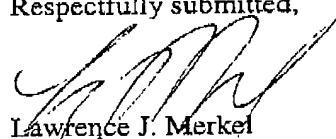
SUBMISSION OF FORMAL DRAWINGS

Official Draftsman
Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings (Figures 1-9 on 8 sheets) be accepted for filing.

Respectfully submitted,



Lawrence J. Merkel

Reg. No. 41,191

AGENT FOR APPLICANTS

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Date. January 16, 2002

